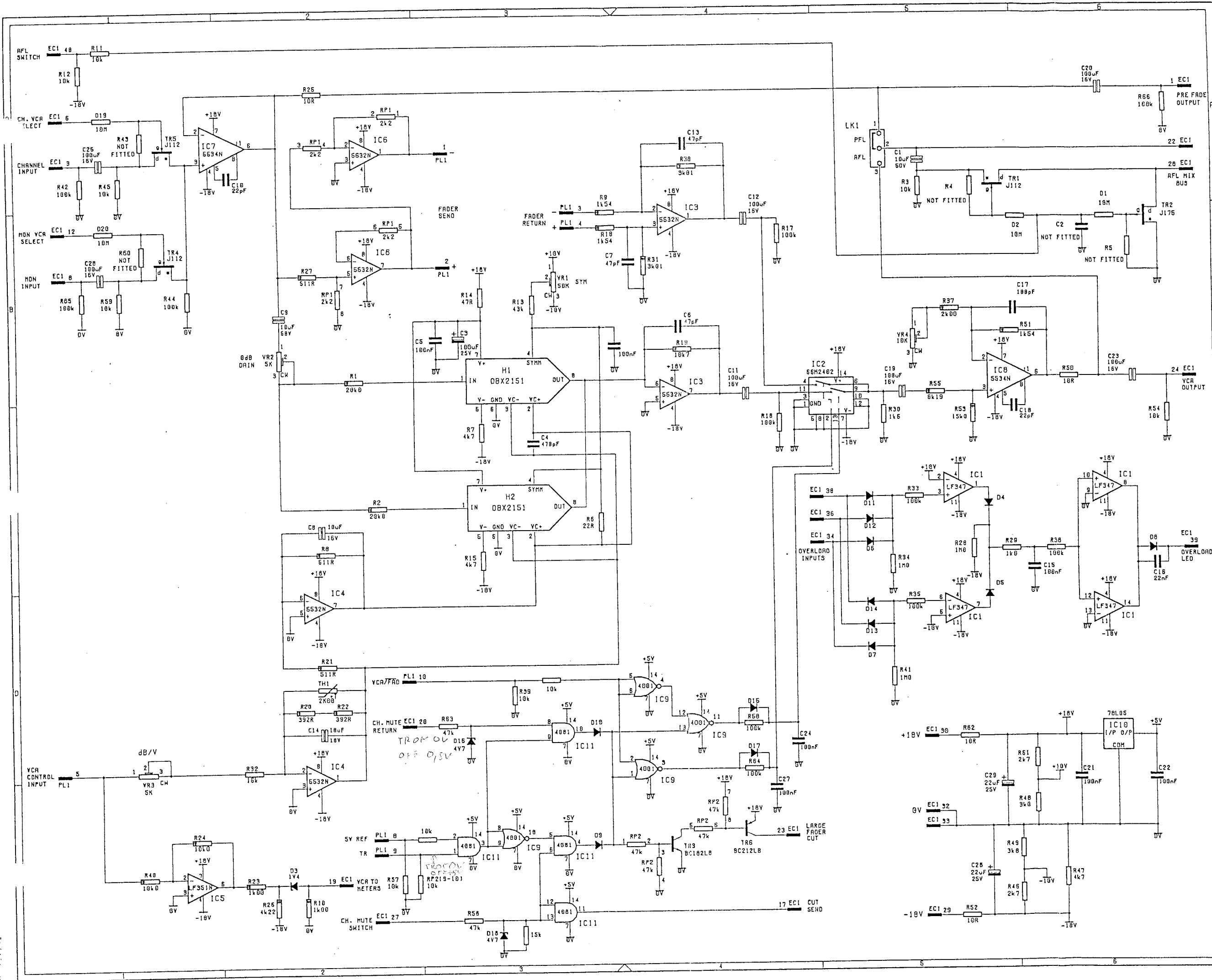




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REV	ISS	DATE	DETAILS	
0	A	22 APR 91	NEW DRAWING	BC MM
1	A	11 JLY 91	ECO 4K/786 R43 R60 WERE 10M NOW NOT FITTED D19 D20 WERE DIODES NOW 10M RESISTORS	BC MM
2	A	11 JLY 91	ECO 4K/794. NO CHANGE	BC MM
3	A	11 JLY 91	ECO 4K/800 VR2 WAS 2K0 NOW 5K0	BC MM
4	A	21 AUG 91	ECO 4K/832 100NF CAPACITOR MM ADDED AT H2 PIN 4	50 MM
5	A	7 OCT 91	ECO 4K/864 R56 R63 WERE 2K7 NOW 47K. RP2 WAS 5x10k NOW 4x47K RP2 (9-10) 10K ADDED. R30 WAS 100K NOW 1K5	BC MM
6	A	29 JAN 92	ECO 4K/912 C8 C14 WERE 1UF NOW 10UF 16V	BC MM
7	A	26 OCT 92	ECO 4K/946 VR2 VR3 NOW FITTED REVERSE TO SCREENING	MM
8	A	26 OCT 92	ECO 4K/1043 R33 R35 WERE 47K NOW 100K R34 R41 WERE 47K NOW 1M	MM
9	A	13 OCT 93	ECO 4K/1060 NO CHANGE	MM
10	A	13 OCT 93	ECO 4K/1198 2 RESISTORS ADDED AT PL1,10 PL1,8	MM
11	A	13 OCT 93	ECO 4K/1247 15K NOW AT IC11,13 TR1 TR2 CIRCUITRY MODIFIED	MM
E2 VERSION				
PCB ISSUE EF				
USED ON		SL678	A2	
TITLE		MOVING FADER CHANNEL VCA		
ORG.NO.		T82354.71.2		
SHEET		1 OF 1		
Solid State Logic				